



# SNS COLLEGE OF ENGINEERING

Kurumbapalayam (Po), Coimbatore – 641 107

**An Autonomous Institution**

Accredited by NBA – AICTE and Accredited by NAAC – UGC with 'A' Grade  
Approved by AICTE, New Delhi & Affiliated to Anna University, Chennai

## DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

**COURSE NAME : 19EE101-BASIC ELECTRICAL & ELECTRONICS ENGINEERING**

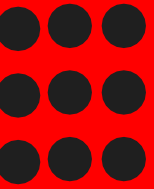
I YEAR /I SEMESTER

Unit 5: Linear and Digital Electronics

Topic : Logic Gates

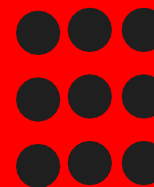


# GRADUATE ATTRIBUTES

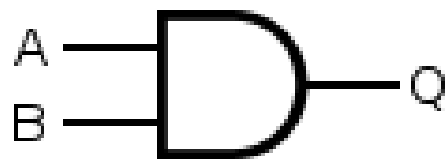




# INTRODUCTION TO LOGIC GATES



A logic gate is an idealized model of computation or physical electronic device implementing a Boolean function, a logical operation performed on one or more binary inputs that produces a single binary output.





# TYPES OF LOGIC GATE

Six types of gates

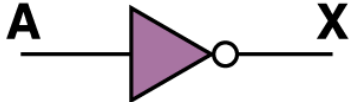
- NOT
- AND
- OR
- XOR
- NAND
- NOR

Typically, logic diagrams are black and white with gates distinguished only by their shape



# NOT GATE

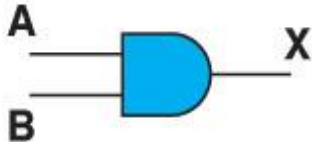
A NOT gate accepts one input signal (0 or 1) and returns the opposite signal as output

Boolean Expression	Logic Diagram Symbol	Truth Table						
$X = A'$		<table border="1"><thead><tr><th>A</th><th>X</th></tr></thead><tbody><tr><td>0</td><td>1</td></tr><tr><td>1</td><td>0</td></tr></tbody></table>	A	X	0	1	1	0
A	X							
0	1							
1	0							



# AND GATE

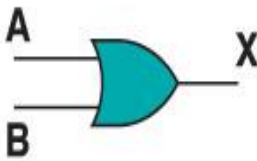
An AND gate accepts two input signals. If both are 1, the output is 1; otherwise, the output is 0.

Boolean Expression	Logic Diagram Symbol	Truth Table															
$X = A \cdot B$		<table border="1"><thead><tr><th>A</th><th>B</th><th>X</th></tr></thead><tbody><tr><td>0</td><td>0</td><td>0</td></tr><tr><td>0</td><td>1</td><td>0</td></tr><tr><td>1</td><td>0</td><td>0</td></tr><tr><td>1</td><td>1</td><td>1</td></tr></tbody></table>	A	B	X	0	0	0	0	1	0	1	0	0	1	1	1
A	B	X															
0	0	0															
0	1	0															
1	0	0															
1	1	1															



# OR GATE

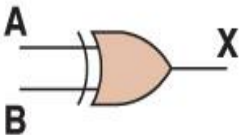
An OR gate accepts two input signals. If both are 0, the output is 0; otherwise, the output is 1.

Boolean Expression	Logic Diagram Symbol	Truth Table															
$X = A + B$		<table border="1"><thead><tr><th>A</th><th>B</th><th>X</th></tr></thead><tbody><tr><td>0</td><td>0</td><td>0</td></tr><tr><td>0</td><td>1</td><td>1</td></tr><tr><td>1</td><td>0</td><td>1</td></tr><tr><td>1</td><td>1</td><td>1</td></tr></tbody></table>	A	B	X	0	0	0	0	1	1	1	0	1	1	1	1
A	B	X															
0	0	0															
0	1	1															
1	0	1															
1	1	1															



# XOR GATE

An XOR gate accepts two input signals. If both are the same, the output is 0; otherwise, the output is 1.

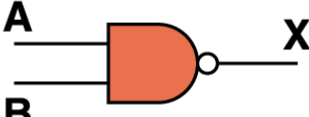
Boolean Expression	Logic Diagram Symbol	Truth Table															
$X = A \oplus B$		<table border="1"><thead><tr><th>A</th><th>B</th><th>X</th></tr></thead><tbody><tr><td>0</td><td>0</td><td>0</td></tr><tr><td>0</td><td>1</td><td>1</td></tr><tr><td>1</td><td>0</td><td>1</td></tr><tr><td>1</td><td>1</td><td>0</td></tr></tbody></table>	A	B	X	0	0	0	0	1	1	1	0	1	1	1	0
A	B	X															
0	0	0															
0	1	1															
1	0	1															
1	1	0															





# NAND GATE


The NAND gate accepts two input signals. If both are 1, the output is 0; otherwise, the output is 1.

Boolean Expression	Logic Diagram Symbol	Truth Table															
$X = (A \cdot B)'$		<table border="1"><thead><tr><th>A</th><th>B</th><th>X</th></tr></thead><tbody><tr><td>0</td><td>0</td><td>1</td></tr><tr><td>0</td><td>1</td><td>1</td></tr><tr><td>1</td><td>0</td><td>1</td></tr><tr><td>1</td><td>1</td><td>0</td></tr></tbody></table>	A	B	X	0	0	1	0	1	1	1	0	1	1	1	0
A	B	X															
0	0	1															
0	1	1															
1	0	1															
1	1	0															



# NOR GATE

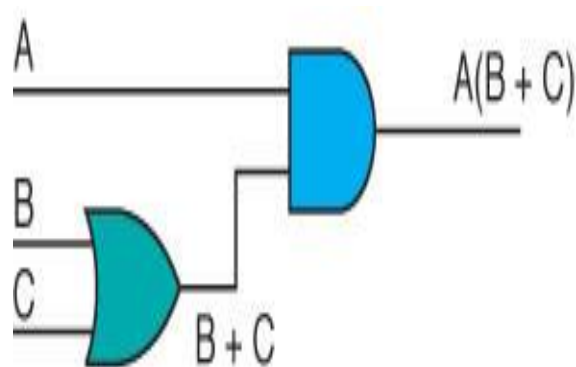
The NOR gate accepts two input signals. If both are 0, the output is 1; otherwise, the output is 0.

Boolean Expression	Logic Diagram Symbol	Truth Table															
$X = (A + B)'$		<table border="1"><thead><tr><th>A</th><th>B</th><th>X</th></tr></thead><tbody><tr><td>0</td><td>0</td><td>1</td></tr><tr><td>0</td><td>1</td><td>0</td></tr><tr><td>1</td><td>0</td><td>0</td></tr><tr><td>1</td><td>1</td><td>0</td></tr></tbody></table>	A	B	X	0	0	1	0	1	0	1	0	0	1	1	0
A	B	X															
0	0	1															
0	1	0															
1	0	0															
1	1	0															



# SAMPLE COMBINATIONAL CIRCUIT

Consider the following Boolean expression  $A(B + C)$



A	B	C	B + C	A(B + C)
0	0	0	0	0
0	0	1	1	0
0	1	0	1	0
0	1	1	1	0
1	0	0	0	0
1	0	1	1	1
1	1	0	1	1
1	1	1	1	1



# REFERENCES

1. Muthusubramanian R, Salivahanan S, “Basic Electrical and Electronics Engineering”, Tata McGraw Hill Publishers, (2009) - UNIT I – V
2. Bhattacharya. S.K, “Basic Electrical and Electronics Engineering”, Pearson Education , (2017) – UNIT I – IV
3. Mehta V K, Mehta Rohit, “Principles of Electrical Engineering and Electronics”, S.Chand & Company Ltd, (2010)- UNIT I and II
4. Mehta V K, Mehta Rohit, “Principles of Electronics”, S.Chand & Company Ltd, (2005)- UNIT IV and V

## THANK YOU