## Assembly language programming

The assembly language is made up of elements which all are used to write the program in sequential manner. Follow the given rules to write programming in assembly language.

## Rules of Assembly Language

- The assembly code must be written in upper case letters
- The labels must be followed by a colon (label:)
- All symbols and labels must begin with a letter
- All comments are typed in lower case
- The last line of the program must be the END directive The assembly language mnemonics are in the form of op-code, such as MOV, ADD, JMP, and so on, which are used to perform the operations.

Op code
Operands

Op-code: The op-code is a single instruction that can be executed by the CPU. Here the opcode is a MOV instruction.

Operands: The operands are a single piece of data that can be operated by the op-code. Example, multiplication operation is performed by the operands that are multiplied by the operand.

## Syntax: MUL a,b;

## The Elements of an Assembly Language Programming:

- Assembler Directives
- Instruction Set
- Addressing Modes

| Operation | Opcode | Operand | Description |
| :--- | :--- | :--- | :--- |


| Addition | ADD | A, Rn | [ A$]<-[\mathrm{A}]+[\mathrm{Rn}]$ |
| :---: | :---: | :---: | :---: |
|  | ADD | A, Address | [A]<-[A]+[ Data at Address] |
|  | ADD | A, @Rn | $[\mathrm{A}]<-[\mathrm{A}]+[$ Data at Address pointed by $\mathrm{Rn}]$ |
|  | ADD | A, \#data | [A]<-[A]+[Data] |
|  | ADDC | A, Rn | [A]<-[A]+[Rn]+[Carry flag] |
|  | ADDC | A, <br> Address | $[\mathrm{A}]<-[\mathrm{A}]+[$ Data at Address] $]$ [Carry flag] |
|  | ADDC | A, @Rn | $[\mathrm{A}]<-[\mathrm{A}]+[$ Data at Address pointed by $\mathrm{Rn}]+[$ Carry flag] |
|  | ADDC | A, \#data | [A]<-[A]+[Data]]+[Carry flag] |
| Subtraction | SUBB | A, Rn | [ A$]<-[\mathrm{A}]-[\mathrm{Rn}]$ |
|  | SUBB | A, Address | [ A$]<-[\mathrm{A}]$-[ Data at Address] |
|  | SUBB | A, @Rn | [A]<-[A]-[ Data at Address pointed by $\mathrm{Rn}]$ |
|  | SUBB | A, \#data | [A]<-[A]-[Data] |
| Increment | INC | A | [ A$]<-[\mathrm{A}+1]$ |
|  | INC | Rn | $[\mathrm{Rn}]<-[\mathrm{Rn}+1]$ |
|  | INC | Address | [Data at Address]<-[Data at Address+1] |
|  | INC | @Rn | [Data at Address pointed by register]<- <br> [Data at Address pointed by register +1 ] |


|  | INC | DPTR | [DPTR]<-[DPTR + 1] |
| :---: | :---: | :---: | :---: |
| Decrement | DEC | A | [A]<-[A-1] |
|  | DEC | Rn | $[\mathrm{Rn}]<-[\mathrm{Rn}-1]$ |
|  | DEC | Address | [Data at Address]<-[Data at Address-1] |
|  | DEC | @ Rn | [Data at Address pointed by register]<- <br> [Data at Address pointed by register-1] |
| Multiplication | MUL | A,B | $[\mathrm{A}]<-[\mathrm{A}] *[\mathrm{~B}]$ |
| Division | DIV | A, B | $[\mathrm{A}]<-[\mathrm{A}] /[\mathrm{B}]$ |
| Decimal adjust | DA | A | Coverts binary addition to BCD |

