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SNS COLLEGE OF TECHNOLOGY

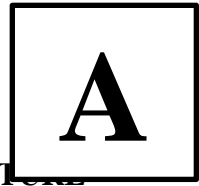
(An Autonomous Institution)

Coimbatore – 641 035.

B.E / B.Tech – Internal Assessment Exam- I

Academic Year 2023-2024 (ODD)

FIFTH SEMESTER (REGULATION R2019)



19ITT202 – COMPUTER ORGANIZATION AND ARCHITECTURE

TIME: 1.5 HOURS

MAXIMUM MARKS: 50

ANSWER ALL QUESTIONS

PART A — (5 x 2 = 10 Marks)

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| 1. | Give a short sequence of machine instructions for the task “Add the contents of R1 and memory location 2500, and place the answer in location C” | CO1 | APP |
| 2. | Give the advantage of cache memory. | CO1 | REM |
| 3. | List down the Various register. | CO1 | REM |
| 4. | What is half adder? | CO2 | REM |
| 5. | What is an overflow? When does it occur? | CO2 | UND |

PART B — (2 x 13 = 26 Marks)

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|-------------|--|----|-----|-----|
| 6. | (a) Discuss in detail about the various measures of performance of a computer. | 13 | CO1 | UND |
| (OR) | | | | |
| | (b) Explain the various components of computer System with neat diagram. | 13 | CO1 | UND |
| 7. | (a) Explain briefly on instruction set and its types with an example of each type. | 13 | CO1 | UND |
| (OR) | | | | |
| | (b) Define Addressing mode and Illustrate the basic addressing modes with an example for each. | 13 | CO1 | REM |
| 8. | (a) Discuss briefly about floating point addition and subtraction algorithms. | 14 | CO2 | UND |
| (OR) | | | | |
| | (b) Summarize briefly about carry-look ahead adder and also give the expression for full adder circuit to show carry generation and propagation. | 14 | CO1 | UND |

Prepared By

Verified By

HOD