





First we will see what are timers, their working and later we will configure the PIC16f877a timers to generate delay of 100ms and 500ms respectively. At the end we will see how to use the Explore Emdedded Timer library.

Timer Basics

As the name suggests these are used to measure the time or generate the accurate time delay. The microcontroller can also generate/measure the required time delays by running loops, but the timer/counter relieves the CPU from that redundant and repetitive task, allowing it to allocate maximum processing time for other tasks.

Timer is nothing but a simple binary counter that can be configured to count clock pulses(Internal/External). Once it reaches the Max value, it will roll back to zero setting up an **OverFlow** flag and generates the interrupt if enabled.







Timer Block Diagram

ExploreEmbedded



Timer 0

The TMR0 module is an 8-bit timer/counter with the following features:

- •8-bit timer/counter
- •Readable and writable
- •8-bit software programmable prescaler
- •Internal or external clock select
- •Interrupt on overflow from FFh to 00h
- •Edge select for external clock

Timer0 Block Diagram Excluding WDT

Below are the steps for configuring and using the Timer0 for delay generation:

- 1.Calculate the Timer Count for the required delay.
- 2.Set the Presaclar bits in **OPTION_REG** as per the delay calculations.
- 3.Clear the **PSA**bit for using the prescalar.
- 4.Select the Clock Source Internal/External using **TOCS** bit.
- 5.Load the timer value into **TMRO** register.
- 6.Enable the Timer0 Interrupt by setting **TMR0IE** bit
- 7. Enable the Global and Peripheral interrupts by setting **GIE** and **PIE** bits

Below is the sample code to blink the LEDs with **1ms** delay.

Timer 1

The timer TMR1 module is an 16-bit timer/counter with the following features:

- •16-bit timer/counter with two 8-Bit register TMR1H/TMR1L
- •Readable and writable
- •software programmable prescaler upto 1:8
- •Internal or external clock select
- •Interrupt on overflow from FFFFh to 00h
- •Edge select for external clock

Below are the steps for configuring and using the Timer1 for delay generation:

1.Calculate the Timer Count for the required delay.

2.Set the Presaclar bits in **T1CON** as per the delay calculations.

3.Select the Clock Source Internal/External using **TMR1CS** bit.

4.Load the timer value into TMR1H,TMR1L register.

5.Enable the Timer1 Interrupt by setting **TMRIE** bit

6.Enable the Global and Peripheral interrupts by

setting GIE and PIE bits

7. Finally start the timer by setting **TMR10N** bit

Below is the sample code to blink the LEDs with **100ms** delay.

TIMER1 BLOCK DIAGRAM

Timer 2

The TImer2 module is an 8-bit timer/counter with the following features:

- •8-bit timer/counter
- •Readable and writable
- •Software programmable prescaler/PostScaler upto 1:16
- •Interrupt on overflow from FFh to 00h

TIMER2 BLOCK DIAGRAM

Below are the steps for configuring and using the Timer2 for delay generation:

- 1.Calculate the Timer Count for the required delay.
- 2.Set the Prescaler bits in **T2CON** as per the delay calculations.

3.Load the timer value into TMR2 register.
4.Enable the Timer2 Interrupt by setting TMR2IE bit
5.Enable the Global and Peripheral interrupts by setting GIE and PIE bits
6.Finally start the Timer2 by setting TMR2ON bit

A watchdog timer (WDT) is a timer that monitors microcontroller (MCU) programs to see if they are out of control or have stopped operating. It acts as a "watchdog" watching over MCU operation.

A microcontroller (MCU) is a compact processor for controlling electronic devices. Integrated into a wide variety of electronic devices, MCUs come preloaded with program software whose commands are used to control electronic devices.

This makes safeguarding normal MCU operation essential. Should the MCU program, for some reason, run out of control or stop running altogether, the electronic device may behave erratically, which in the worst case could cause damage or an accident.

To proactively prevent such incidents, it falls to the role of the watchdog timer to constantly watch over the MCU to ensure it is operating normally.

The watchdog timer function can be inside the MCU, but here we are introducing "external" watchdog timers, the safer kind.

The watchdog timer communicates with the MCU at a set interval. If the MCU does not output a signal, outputs too many signals or outputs signals that differ from a predetermined pattern, the timer determines that the MCU is malfunctioning and sends a reset signal to the MCU.

Time-out mode, the watchdog timer determines the MCU is malfunctioning and outputs a reset signal if it does not receive a signal from the MCU within the set interval.

The time-out mode is a major WDT monitoring mode or method, but it sometimes fails to detect MCU faults.

In Time-out mode, the WDT will not detect an MCU fault if the MCU inputs multiple signals (= double pulse) in the set period.

When time-out mode will not detect a MCU failure

The **window mode** enables more accurate detection of faults than the Time-out mode.

In window mode, the watchdog timer determines that the MCU is malfunctioning and outputs a reset signal **if it does not receive a signal, or receives multiple signals (= double pulse) from the MCU within the set interval.**

A window mode watchdog timer may be more suitable for applications such as automotive devices that require greater safety.

WDT operation (Window mode)

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