



SNS COLLEGE OF TECHNOLOGY

Coimbatore-35
An Autonomous Institution



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Approved by AICTE, New Delhi & Affiliated to Anna University, Chennai

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

19ECB211 – Microcontroller Programming & Interfacing

II YEAR/ IV SEMESTER

UNIT 5 – Advanced Microcontrollers

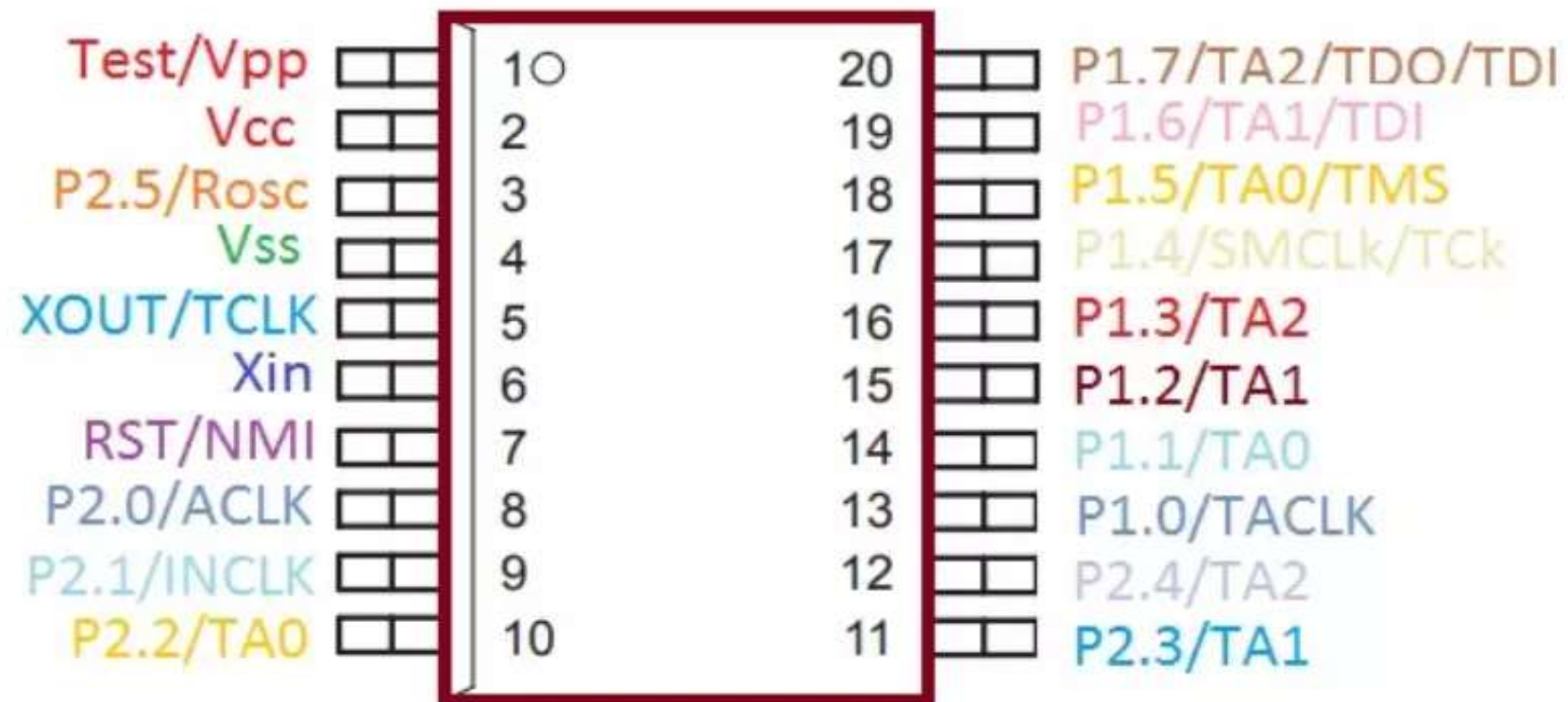
TOPIC 2 – MSP430 device pin out



MSP430X22X2 device pin out



MSP430 Pinout



www.TheEngineeringProjects.com



MSP430X22X2 device pin out



Pin#	Type	Parameters
Pin#13	P1.0/TACLK	It is general-purpose digital I/O pin / Timer_A, clock signal TACLK input.
Pin#14	P1.1/TA0	It is general-purpose digital I/O pin/Timer_A, Capture: CCI0A input, Compare: Out0 output.
Pin#15	P1.2/TA1	It is general-purpose digital I/O pin/Timer_A, Capture: CCI1A input, Compare: Out1 output.
Pin#16	P1.3/TA2	It is general-purpose digital I/O pin/Timer_A, Capture: CCI2A input, Compare: Out2 output.
Pin#17	P1.4/SMCLK/TCK	It is general-purpose digital I/O pin/SMCLK signal output/Test clock, an input terminal for device programming and test.
Pin#18	P1.5/TA0/TMS	It is general-purpose digital I/O pin/Timer_A, Compare: Out0 output/test mode select, an input terminal for device programming and test.
Pin#19	P1.6/TA1/TDI	It is general-purpose digital I/O pin/Timer_A, Compare: Out1 output/test data input terminal.
Pin#20	P1.7/TA2/TDO/TDI	It is general-purpose digital I/O pin/Timer_A, Compare: Out2 output/test data output terminal or data input during programming.
Pin#8	P2.0/ACLK	It is general-purpose digital I/O pin/ACLK output.
Pin#9	P2.1/INCLK	It is general-purpose digital I/O pin/Timer_A, a clock signal at INCLK.
Pin#10	P2.2/TA0	It is general-purpose digital I/O pin/Timer_A, Capture: CCI0B input, Compare: Out0 output.



MSP430X22X2 device pin out



Pin#	Type	Parameters
Pin#12	P2.4/TA2	It is general-purpose digital I/O pin/Timer_A, Compare Out2 output.
Pin#3	P2.5/ROSC	It is general-purpose digital I/O pin/Input for an external resistor that defines the DCO nominal frequency.
Pin#7	RST/NMI	It is Reset or nonmaskable interrupt input.
Pin#1	TEST/VPP	It is selected test mode for JTAG pins on Port1/programming voltage input during EPROM programming.
Pin#2	VCC	It is a Supply voltage.
Pin#4	VSS	It is Ground reference.
Pin#6	XIN	It is an Input terminal of the crystal oscillator.
Pin#5	XOUT/TCLK	The output terminal of a crystal oscillator or test clock input.



THANK YOU