

**19CSB201 - Operating Systems**

**UNIT II**

**MCQ.** When a thread needs to wait for an event it will

- A. Block
- B. Execute
- C. Terminate
- D. Update

ANSWER:A

**MCQ.** In a pure Kernel Level Thread facility all of work of thread management is done by the

- A. Application
- B. Program
- C. Kernel
- D. Threads

ANSWER:C

**MCQ.** Microkernel design imposes a uniform

- A. Process
- B. Processor
- C. Interface
- D. System

ANSWER:C

**MCQ.** Microkernel architecture facilitates

- A. Functionality
- B. Extensibility
- C. Reliability
- D. Portability

ANSWER:B

**MCQ.** Asynchronous elements in program can be implemented through

- A. Bugs
- B. Threads
- C. Programs
- D. Security Policy

ANSWER:B

**MCQ.** Unit of dispatching is usually referred as

- A. Multiprogramming
- B. Security Policy
- C. Problem
- D. Thread

ANSWER:D

**MCQ.** A single machine instruction can controls simultaneous execution of a number processing elements on

- A. Lockstep Basis
- B. Open Step Basis
- C. Early Basis
- D. None

ANSWER:A

**MCQ.** Owner of an address space can grant a number of its

- A. Modules
- B. Pages
- C. Devices
- D. Computers

ANSWER:B

**MCQ.** Microkernel lends itself to

- A. Computers
- B. Systems
- C. Distributed Systems
- D. Applications

ANSWER:C

**MCQ.** Microkernel supports

- A. Flexibility
- B. Reliability
- C. Accessible
- D. Rigid

ANSWER:A

**MCQ.** In Many-to-One multithreading model many user level threads are attached

to

- A. One register
- B. operating system
- C. One kernel thread
- D. other threads

ANSWER:C

**MCQ.** Programs involve a variety of activities is easier to design and implement using

- A. Programs
- B. Information
- C. Data
- D. Threads

ANSWER:D

**MCQ.** Multiple threads within same process may be allocated to separate

- A. Applications
- B. Programs
- C. Processors
- D. Processes

ANSWER:C

**MCQ.** A lightweight process (LWP) can be viewed as a mapping between ULTs and

- A. Kernel Threads
- B. UNIX Thread
- C. Linux Threads
- D. Window Vista Threads

ANSWER:A

**MCQ.** Modular design helps to enhance

- A. Functionality
- B. Reliability
- C. Portability
- D. Flexibility

ANSWER:B

**MCQ.** Microkernel is responsible for mapping each virtual

- A. Module
- B. I/O Devices
- C. Memory

D. Page

ANSWER:D

**MCQ.** Thread processor affinity is set of

- A. Processes
- B. Processors
- C. Programs
- D. Applications

ANSWER:B

**MCQ.** In UNIX, thread is

- A. Runnable
- B. Executing
- C. Updated
- D. Access

ANSWER:A

**MCQ.** When a process is spawned, a thread for that process is also

- A. Complete
- B. Spawned
- C. Closed
- D. Update

ANSWER:B

**MCQ.** A sequence of data is transmitted to a set of

- A. Devices
- B. Resources
- C. Computers
- D. Processors

ANSWER:D