

SNS COLLEGE OF TECHNOLOGY (AN AUTONOMOUS INSTITUTION)

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Department of Biomedical Engineering

Course Name: 23BMB101-Electron Devices and Circuits

I Year : II Semester

Unit II –**Transistors**

Topic : Junction Field Effect Transistor

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INTRODUCTION

- FET is an acronym used for "field effect transistor". It is a three terminal unipolar device in which conduction is manipulated with the help of applied electric field.
- It is also referred as a voltage controlled device in which only majority charge carriers are involved in the conduction mechanism. It comprises of three terminals, i.e. source, gate, and drain.







Water Analogy for Understanding FET



Classification of FET



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on Title 3



Junction Field Effect Transistor

JFET is Junction gate field-effect transistor. Normal transistor is a current controlled device which needs current for biasing, whereas JFET is a voltage controlled device.











Construction





Working of JFET-When NO bias is applied





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Working of JFET-When small negative bias is applied





JFET with Small Negative Bias

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Working of JFET-When the Large negative bias is applied





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Characteristics of JFET-Output Characteristics or Drain Characteristics





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Vgs = 0	Region
Pinch – Off Point	
 Knee point 	
	i

JFET Output Characteristics with Shorted gate

Output Characteristics

nee Point: There exists a point in the characteristics curve where the variation of drain current with drain-source voltage appears to be linear. But after this point, the linearity changes into a curve.

- **Channel Ohmic Region**: The region to the left of the knee point in the characteristics curve is the channel ohmic region.
- **Pinch-off point:** The point in the curve above which the drain current does not increases further no matter how much we are increasing the drain to source voltage, this point is termed as the pinch-off point. 10
- **Pinch-off Voltage:** The voltage at the pinch-off point is termed as pinch-off voltage because at this voltage the current is completely turned to be constant. **Drain-Source Saturation Current:** The drain to source saturation current is the current which becomes constant or completely enters a saturation state 23BMB101/EDC/Dr.R.Karthick/HoD/BME



Output Characteristics – With external bias:





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Gate channel junction Breakdown Region	
	H
	Π
	٢
Vds max	
	Gate channel j Breakdown F

JFET Output Characteristics with External Bias



Transfer Characteristics



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